

Atty Docket 03-0157 (4028-03100)

In the Specification:

Please delete Paragraph 44 and substitute the following paragraph therefor.

[0044] In one embodiment, the instruction sequencing unit 402 translates instruction operation codes (i.e., opcodes) into native opcodes for the processor. The instruction sequencing unit 402 checks the multiple decoded instructions using grouping and dependency rules and provides (i.e., issues) one or more of the decoded instructions conforming to the grouping and dependency rules as a group to the load/store (LSU) and/or execution unit 406 for simultaneous execution.